Application No.	Applicant(s)
10/614,504	FREDERICK A. PERNER ET AL.
Examiner	Art Unit
Gene N Auduong	2818
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.	
1. This communication is responsive to <u>appli9cation filed July 7, 2003</u> .	
3. The drawings filed on <u>07 July 2003</u> are accepted by the Examiner.	
4.	
6. ☐ Interview Summar Paper No./Mail D 8), 7. ☐ Examiner's Amen	ate
	Examiner Gene N Auduong ars on the cover sheet with the COR REMAINS) CLOSED in this a cor other appropriate communication GHTS. This application is subject and MPEP 1308. 7, 2003. aminer. der 35 U.S.C. § 119(a)-(d) or (f). been received. been received in Application No communication to file a replent of this communication to file a replent of this application. tted. Note the attached EXAMINE is reason(s) why the oath or declar to be submitted. on's Patent Drawing Review (PTC is Amendment / Comment or in the series have been received in the drawner had been according to 37 CFR 1.12 is of BIOLOGICAL MATERIAL FOR THE DEPOSIT OF BIOLOGICAL MATERIAL FOR THE PATERIAL FOR THE PA

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Information Disclosure Statement

1. The information disclosure statement (IDS) submitted on July 7, 2003 is being considered by the examiner.

Allowable Subject Matter

- 2. Claims 1-21 are allowed.
- 3. The following is an examiner's statement of reasons for allowance: The prior art of record does not show or suggest, in addition to other element in the claim, claiming the structure and function for the circuit as claimed: a memory system having a memory cell string that includes a first memory cell coupled to a second memory cell; a sense amplifier coupled to the memory cell string and configured to: detect a first voltage at a node between the first and second memory cells at a first time; detect a second voltage at the node at a second time subsequent to the first memory cell being written to a first state; compare the first and second voltages; and cause a logic level associated with the first memory cell to be stored in response to comparing the first and second voltages.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Gene N Auduong whose telephone number is (571) 272-1773.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571) 272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

GA September 1, 2004

> Gene N Auduong Primary Examiner Art Unit 2818

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